

**PRELIMINARY** 

Data Sheet June 20, 2005

FN6117.0

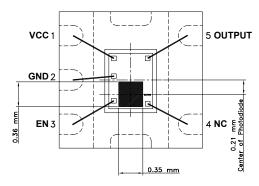
### Ambient Light Photo Detect IC

The ISL29000 is a light-to-current optical sensor combining a photodiode and a current amplifier on a single monolithic IC. Output current is directly proportionate to the light intensity on the photodiode. Its sensitivity is superior to that of a phototransistor and exhibits little variation. Its spectral sensitivity matches closely to the luminous efficiency and linearity.

Housed in an ultra-compact surface mount clear plastic package, this device is excellent for power saving control function in cell phones, PDAs, and other handheld applications.

#### **Pinout**

ISL29000 (5-PIN DFN) TOP VIEW



#### Features

- · Monolithic IC containing photodiode and amplifier
- · Converts light intensity to current
- · 2.5V to 5.5V supply range
- · Low supply current 1µA
- · Excellent output linearity of luminance
- · Ultra-compact and light surface mount package
- · Pb-Free plus anneal available (RoHS compliant)

## **Applications**

- · Mobile phones
- Notebook PCs
- PDAs
- · Video cameras
- · Digital cameras

### **Ordering Information**

PART NUMBER	PACKAGE	TAPE & REEL	PKG. DWG. #
ISL29000IROZ (See Note)	5-Pin ODFN (Pb-free)	-	MDP0052

NOTE: Intersil Pb-free plus anneal products employ special Pb-free material sets; molding compounds/die attach materials and 100% matte tin plate termination finish, which are RoHS compliant and compatible with both SnPb and Pb-free soldering operations. Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.

#### **Absolute Maximum Ratings** (T<sub>A</sub> = 25°C)

Supply Voltage between V <sub>SD</sub> and GND 6V	Maximum Die Temperature
Maximum Continuous Output Current TBD	Storage Temperature65°C to +150°C
Operating Temperature40°C to +85°C	

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

IMPORTANT NOTE: All parameters having Min/Max specifications are guaranteed. Typical values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore:  $T_J = T_C = T_A$ 

 $\textbf{Electrical Specifications} \hspace{0.5cm} V_{CC} = 3V, \, T_{A} = 25^{\circ}C, \, \text{fluorescent light, unless otherwise specified.}$ 

PARAMETER	DESCRIPTION	CONDITION	MIN	TYP	MAX	UNIT
I <sub>CC</sub>	Supply Current	$R_L = 1k\Omega$ , EV = 1000lx		74		μΑ
		EV = 0		0.2		μΑ
I <sub>L1</sub>	Light Current	EV = 1000lx	45	61	75	μA
I <sub>L2</sub>	Light Current	EV = 100lx		6.5		μA
I <sub>LEAK</sub>	Dark Current	EV = 0		0.06		μA
V <sub>O-MAX</sub>	Maximum Output Compliance Voltage	At 95% of normal output current, EV = 1000lx		2.7		V
T <sub>R</sub>	Rise Time (See Note)	$R_L = 5k\Omega$ , EV = 1000lx		27	50	μs
T <sub>F</sub>	Fall Time (See Note)	$R_L = 5k\Omega$ , EV = 1000lx		78	110	μs
T <sub>D</sub>	Delay Time for Rising Edge (See Note)	$R_L = 5k\Omega$ , EV = 1000lx		80	110	μs
T <sub>S</sub>	Delay Time for Falling Edge (See Note)	$R_L = 5k\Omega$ , EV = 1000lx		35	50	μs
$V_{LO}$	Maximum Voltage at EN Pin to Enable				0.6	V
V <sub>HI</sub>	Minimum Voltage at EN Pin to Disable		1.8			V

NOTE: Switching time measurement is based on Figures 1 and 2.

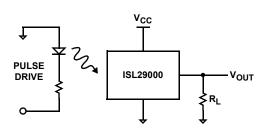


FIGURE 1. RISE/FALL TIME MEASUREMENT

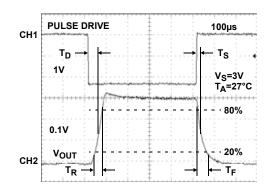


FIGURE 2.

## **Typical Performance Curves**

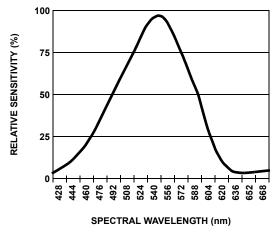


FIGURE 3. RELATIVE SENSITIVITY

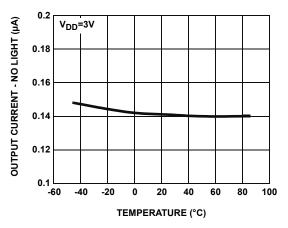


FIGURE 5. DARK CURRENT vs TEMPERATURE

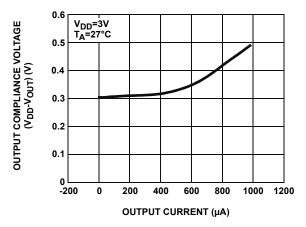


FIGURE 7. OUTPUT COMPLIANCE VOLTAGE vs CURRENT

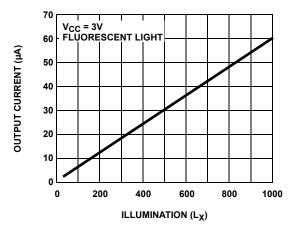


FIGURE 4. SENSITIVITY

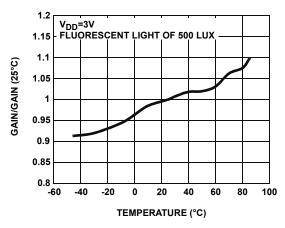
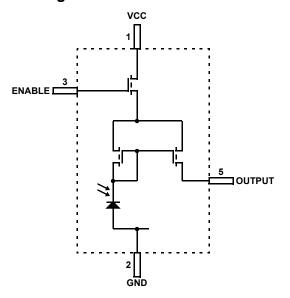


FIGURE 6. GAIN vs TEMPERATURE

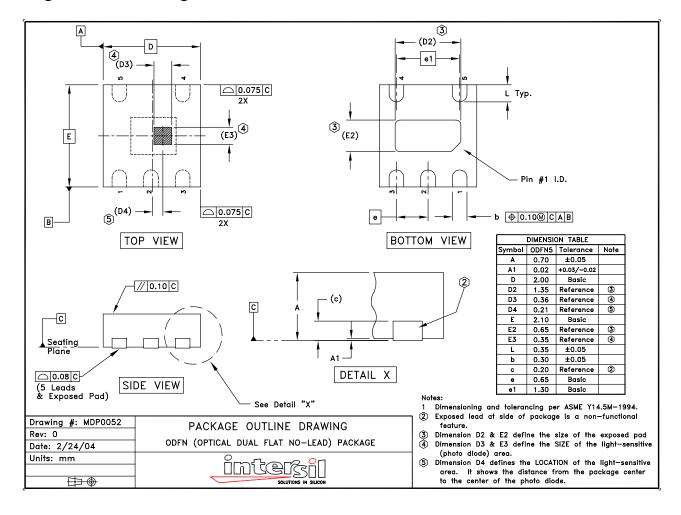
# Pin Descriptions

PIN	NAME	DESCRIPTION
1	VCC	Supply, 2.5V to 5.5V
2	GND	Ground
3	EN	Enable
4	NC	No connect
5	Output	Current output pin

# Block Diagram



### Package Outline Drawing



NOTE: The package drawing shown here may not be the latest version. To check the latest revision, please refer to the Intersil website at http://www.intersil.com/design/packages/index.asp

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June 20, 2005